

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Kiran V. Chatty et al.

Application No.: 10/709,041

Confirmation No.: 3040

Filed: April 8, 2004

Art Unit: 2815

For: LOW TRIGGER VOLTAGE ESD NMOSFET  
TRIPLE-WELL CMOS DEVICES

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Examiner: M. E. Warren

**RESPONSE UNDER 37 C.F.R. 1.112 (NON-FINAL OFFICE ACTION)**

**MS Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated March 21, 2007, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.